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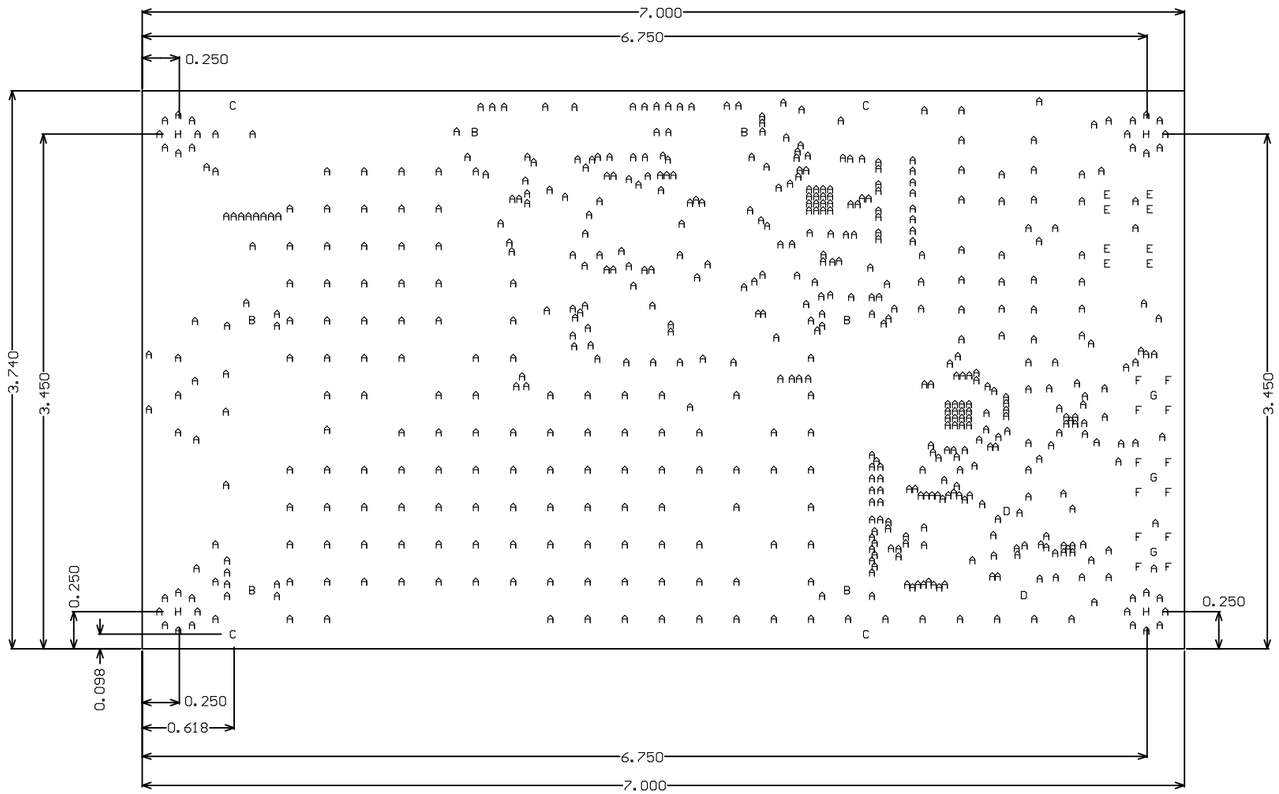
REV	DESCRIPTION	INITIALS	DATE
-1A	Engineering release.	OV	5-31-12
-2A	Initial release.	AC	2-06-13

General Notes:

All dimensions are in inches unless otherwise specified.

Tolerance on all dimensions shall be ±.005" unless otherwise specified.



Symbol	Hit Count	Tool Size	Plated	Hole Type
A	595	15mil (0.381mm)	PTH	Round
B	6	35.433mil (0.9mm)	NPTH	Round
E	8	40mil (1.016mm)	PTH	Round
D	2	50mil (1.27mm)	PTH	Round
G	3	62mil (1.575mm)	PTH	Round
F	12	67mil (1.702mm)	PTH	Round
C	4	114.173mil (2.9mm)	PTH	Round
H	4	156.3mil (3.97mm)	NPTH	Round
	634 Total			

Layer Stack Up Details

Layer Name	Gerber Document	Copper Thickness	Dielectric Height	Dielectric Material
Top Solder Mask	(.GTS)		0.5mil min	Solder Resist
Top Layer	(.GTL)	1.0oz		
			6mil	370HR or Equivalent
InternalPlane1	(.GP1)	1.0oz		
			43.6mil	370HR or Equivalent
InternalPlane2	(.GP2)	1.0oz		
			6mil	370HR or Equivalent
Bottom Layer	(.GBL)	1.0oz		
Bottom Solder Mask	(.GBS)		0.5mil min	Solder Resist

Fabrication Notes:

1. Fabricate printed circuit board per IPC 6012 Class 2.
2. Board material: 370HR or equivalent (exclusive IS410) laminate, 0.062" nominal finished thickness.
3. Copper plating: 1.0oz on all layers and 0.001" min in PTH.
4. Lead Free HASL.
5. Solder mask: LPI, 2-sides, green. Silkscreen: 2-sides, white.
6. All boards shall be 100% electrical tested by design netlist (IPC-D-356A).
7. Add 0.5" rails (with V-score) if board manufactured not in array

ENGINEER: Mike Williamson	TITLE: Indy_Analog_Expansion		
PCB DESIGNER: Oscar Valera			
DATE: 6/5/2013	PART NO.: 90-000173R-2	REV: A	SIZE: B
FILE NAME: Indy_analog_expansion.PcbDoc	PRINT NAME: Drill Drawing	SCALE: SCALE: 0.98	

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